

S2 1 PN="US 5887196"
?t s2/9

2/9/1

DIALOG(R)File 345:Inpadoc/Fam.& Legal Stat
(c) 2001 EPO. All rts. reserv.

15093581

Basic Patent (No,Kind,Date): US 5887196 A 990323 <No. of Patents: 001>

PATENT FAMILY:

UNITED STATES OF AMERICA (US)

Patent (No,Kind,Date): US 5887196 A 990323

SYSTEM FOR RECEIVING A CONTROL SIGNAL FROM A DEVICE FOR SELECTING ITS
ASSOCIATED CLOCK SIGNAL FOR CONTROLLING THE TRANSFERRING OF
INFORMATION VIA A BUFFER (English)

Patent Assignee: APPLE COMPUTER (US)

Author (Inventor): ROSKOWSKI STEVEN G (US); DRAKO DEAN M (US); KREIN
WILLIAM T (US)

Priority (No,Kind,Date): US 185275 A 940124; US 815696 B1 911230

Applic (No,Kind,Date): US 185275 A 940124

National Class: * 395871000; 395823000

IPC: * G06F-013/00

Derwent WPI Acc No: ; G 99-228867

Language of Document: English

UNITED STATES OF AMERICA (US)

Legal Status (No,Type,Date,Code,Text):

US 5887196 P 911230 US AA PRIORITY

US 815696 B1 911230

US 5887196 P 940124 US AE APPLICATION DATA (PATENT)
(APPL. DATA (PATENT))

US 185275 A 940124

US 5887196 P 990323 US A PATENT

Laufer 5887196

?us5887196/pn

Term not in index/PN-LITA : US5887196

LGST	1
CRXX	1
LITA	0
PAST	1

** SS 3 : Results 3

Search statement 4

?prt ss3 max 1-3

1/3 LGST (1/1) - (C) LEGSTAT
PN - US 5887196 [US5887196]
AP - US 185275/94 19940124 [1994US-0185275]
DT - US-P
ACT - 19940124 US/AE-A
APPLICATION DATA (PATENT)
{US 185275/94 19940124 [1994US-0185275]}
- 19990323 US/A
PATENT
UP - 1999-21

2/3 CRXX (1/1) - (C) CLAIMS/RRX
AN - 3128467
PN - 5,887,196 A 19990323 [US5887196]
PA - Apple Computer Inc
PT - E (Electrical)
ACT - 20010322 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20011113
REISSUE REQUEST NUMBER: 09/815873
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2782

Reissue Patent Number:

UP - 2001-46
UACT- 2001-11-13

3/3 PAST (1/1) - (C) PAST
AN - 200146-001727
PN - 5887196 A [US5887196]
DT - A (UTILITY)
OG - 2001-11-13
CO - REA
ACT - REISSUE APPLICATION FILED
SH - REISSUE APPLICATION FILED

Shirelle Green EIC 2100

5,887,196

<=2> GET 1st DRAWING SHEET OF 4

Mar. 23, 1999

System for receiving a control signal from a device for
selecting its associated clock signal for controlling the
transferring of information via a buffer

REISSUE: Reissue Application filed Mar. 22, 2001 (O.G. Nov. 13, 2001) Ex. Gp.:
2782; Re. S.N. 09/815,873

CORE TERMS: clock, buffer, destination, transferred, synchronization,
synchronized, storing, timing, transferring, computer system...

LEXIS-NEXIS
Library: PATENT
File: ALL

5,887,196 OR 5887196

Your search request has found no CASES.

To edit the above request, use the arrow keys. Be sure to move the cursor to the end of the request before you enter it.

To enter a new search request, type it and press the ENTER key.

What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

LEXIS-NEXIS
Library: PATENT
File: CASES

5,887,196 OR 5887196

Your search request has found no ITEMS.

To edit the above request, use the arrow keys. Be sure to move the cursor to the end of the request before you enter it.

To enter a new search request, type it and press the ENTER key.

What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

LEXIS-NEXIS
Library: PATENT
File: JNLS

5,887,196 OR 5887196

Your search request has found no STORIES.

To edit the above request, use the arrow keys. Be sure to move the cursor to the end of the request before you enter it.

To enter a new search request, type it and press the ENTER key.

What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

LEXIS-NEXIS
Library: NEWS
File: CURNWS